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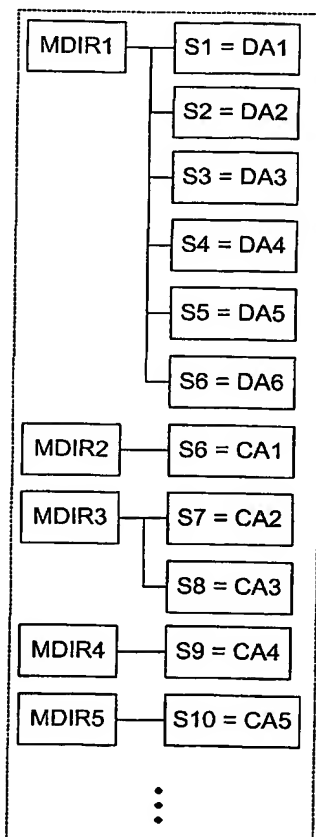
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(54) Title: MODULE FOR READING A DATA CARRIER



(57) Abstract: A module for reading a data carrier, with a processor arrangement and a memory arrangement, • wherein the module is designed for incorporation in a data processing device, • wherein the data carrier comprises data sequences and information on the data sequences, and the data sequences are stored in a directory structure with a root directory and at least one subdirectory, and • wherein the processor arrangement is designed for - writing the information about a first subset of the data sequences present in the root directory into a first directory of the memory arrangement, and - writing the information about at least a second subset of the data sequences present in a subdirectory of the data carrier into a second directory of the memory arrangement.

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